

Refine Search

Search Results -

Terms	Documents
L2.clm.	4

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Thursday, September 22, 2005 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=PGPB; PLUR=YES; OP=OR</i>		
<u>L3</u>	l2.clm.	4	<u>L3</u>
<u>L2</u>	L1 same (serial adj1 bus) same (host or CPU or computer or processor)	9	<u>L2</u>
<u>L1</u>	((dual adj1 mode) or mode) near3 (microcontroller or (micro adj1 controller) or controller)	7888	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 same (serial adj1 bus) same (host or CPU or computer or processor)	9

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Search:

L2	Refine Search
<input type="text" value="L2"/>	<input type="button" value="Refine Search"/>
<input type="button" value="Recall Text"/>	<input type="button" value="Clear"/>
	<input type="button" value="Interrupt"/>

Search History

DATE: Thursday, September 22, 2005 [Printable Copy](#) [Create Case](#)

Set
Name Query
 side by
 side

DB=PGPB; PLUR=YES; OP=OR

		<u>Hit</u>	<u>Set</u>
		<u>Count</u>	<u>Name</u>
<u>L2</u>	L1 same (serial adj1 bus) same (host or CPU or computer or processor)	9	<u>L2</u>
<u>L1</u>	((dual adj1 mode) or mode) near3 (microcontroller or (micro adj1 controller) or controller)	7888	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(439/55 439/527 361/737 361/748 361/684 709/253 370/431 370/465 710/305 710/1 710/315 710/313 710/14 710/11 710/63 710/100 710/301 710/303 710/74 711/115 711/103 711/154 713/100).ccls.	15597

Database:

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Search:

L1

Refine Search

Recall Text  Clear  Interrupt

Search History

DATE: Thursday, September 22, 2005 [Printable Copy](#) [Create Case](#)

[Set](#)[Name Query](#)side by
side

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L1 710/305,1,315,313,14,11,63,100,301,303,74;711/115,103,154;713/100;370/431,465;709/253;361/737,748,684;4

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 and L3	10

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
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JPO Abstracts Database
Derwent World Patents Index
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Search:

L4

Refine Search

Recall Text  Clear Interrupt

Search History

DATE: Thursday, September 22, 2005 [Printable Copy](#) [Create Case](#)

[Set](#)[Name Query](#)

side by
side

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L4 11 and L3

L3 12 same (serial adj1 bus) same (host or CPU or computer or processor)

L2 ((dual adj1 mode) or mode) near3 (microcontroller or (micro adj1 controller) or controller)

L1 710/305,1,315,313,14,11,63,100,301,303,74;711/115,103,154;713/100;370/431,465;709/253;361/737,748,684;4

END OF SEARCH HISTORY